

ABSTRACT

Systems, methodologies, media, and other embodiments associated with simulating a processor performance state in a processor by manipulating an ACPI throttling register are described. One exemplary system embodiment includes a data structure populated with bit pattern data and register address data. The example system may also include a logic for handling a request to produce a processor performance state in a processor and for simulating the processor performance state by causing the processor to be throttled by writing a bit pattern to the ACPI throttling register.